

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>656</b>	<b>((361/790,803).cccls.</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 14:09</b>
<b>2</b>	<b>59</b>	<b>((361/790,803).cccls.) and (memor\$3 and microprocessor\$1)</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 14:16</b>
<b>3</b>	<b>256</b>	<b>((361/790,803).cccls.) and stack\$4</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 14:16</b>
<b>4</b>	<b>227</b>	<b>((361/790,803).cccls.) and stack\$4) not (((361/790,803).cccls.) and (memor\$3 and microprocessor\$1))</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 14:18</b>
<b>5</b>	<b>35</b>	<b>((361/790,803).cccls.) and stack\$4) not (((361/790,803).cccls.) and (memor\$3 and microprocessor\$1))) and (microprocessor\$1 processor\$1)</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 14:24</b>
<b>6</b>	<b>355</b>	<b>vigushin.xa.</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 14:24</b>
<b>7</b>	<b>50</b>	<b>vigushin.xa. and (memor\$3 and (processor\$1 microprocessor\$1))</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 15:10</b>
<b>8</b>	<b>891</b>	<b>257/686.cccls.</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 15:10</b>
<b>9</b>	<b>171</b>	<b>257/686.cccls. and (memor\$3 and (processor\$1 microprocessor\$1))</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 15:10</b>
<b>10</b>	<b>152</b>	<b>(257/686.cccls. and (memor\$3 and (processor\$1 microprocessor\$1))) not (((361/790,803).cccls.) and (memor\$3 and microprocessor\$1)) (vigushin.xa. and (memor\$3 and (processor\$1 microprocessor\$1))))</b>	<b>USPAT; US-PGPUB</b>	<b>2003/02/04 16:17</b>